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PATENT

5298-02502/PM98019/D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor Applicant:
Sethuraman et al.Serial No. 09/779,123
Filed: February 7, 2001Patent No. 6,849,946
Issued: February 1, 2005For: PLANARIZED SEMICONDUCTOR
INTERCONNECT TOPOGRAPHY AND
METHOD FOR POLISHING A METAL
LAYER TO FORM INTERCONNECT

Atty. Docket No. 5298-02502

Group Art Unit: 2823

I hereby certify that this correspondence is being deposited
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11/2/05
DatePamela Gerik
Pamela GerikTRANSMITTAL LETTERCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

Dear Sir:

Transmitted herewith for filing in the captioned case are the following:

- (1) Form PTO-1050, **submitted in duplicate**. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.
- (2) A return postcard to acknowledge receipt of these materials. Please stamp and return this postcard to the undersigned.

All of the errors shown in PTO-1050 are due to Patent Office oversights. A Certificate of Correction is requested under 35 U.S.C. § 254. Applicants believe that no fees are required, however, should any fees be required, please deduct them from deposit account number 50-3268/5298-02502.

Respectfully submitted,

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P.O. Box 684908
Austin, TX 78768-4908
(512) 476-1400Date: October 27, 2005**Certificate**
NOV 10 2005
of Correction

NOV 16 2005

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. 6,849,946

DATED: February 1, 2005

INVENTOR(S): Sethuraman et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims:

Claim 3 at col. 9, line 28: Please delete "relatively narrow."

Claim 12 at col. 10, lines 10-11: Please delete "conducts" and substitute therefor --conductors--.

Claim 18 at col. 10, lines 48: Please delete "conducts" and substitute therefor --conductors--.

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PATENT NO. 6,849,946

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UNITED STATES PATENT AND TRADEMARK OFFICE

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